

MIMAS

DVT BUILD

2010-09-15

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2008/10/01	2008-ECO-016553	B
DATE	CHANGE NO.	REV

	EE	DATE	POWER	DATE	INVENTEC		
DRAWER	Bai, Brain	2008-AGU-25			TITLE MIMAS		
DESIGN	Bai, Brain	2008-AGU-25					
CHECK	Bai, Brain	2008-AGU-25			SIZE = 3 FILE NAME : XXXX-XXXXXX-XX P/N 6050A2215101		
RESPONSIBLE							
					VER : A04	SIZE A3	CODE CS
						DOC. NUMBER 6050A2350601	REV X02
						SHEET 1	OF 34

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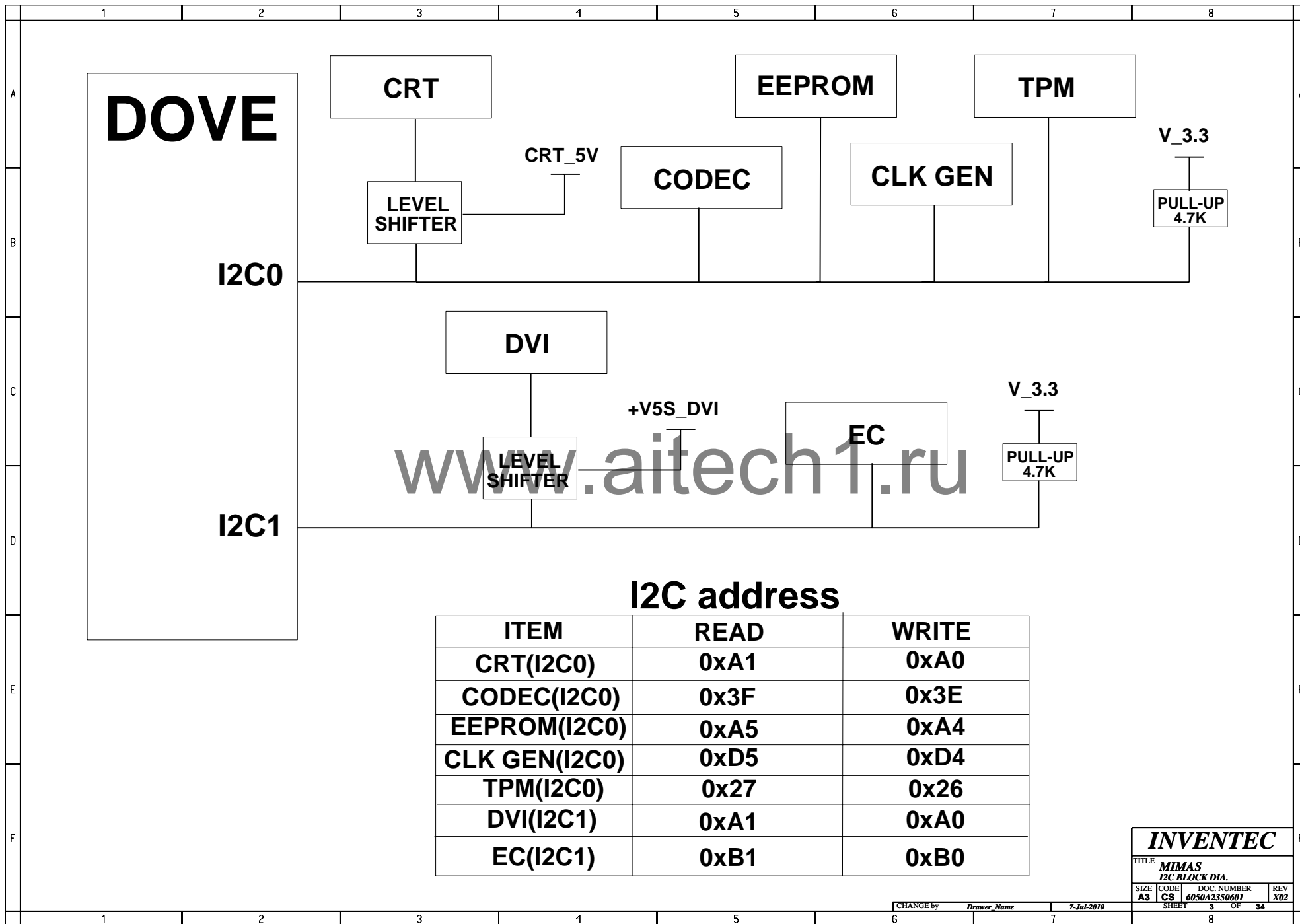
	PAGE
1. COVER PAGE	
2. INDEX	
3. I2C BLOCK DIAGRAM	
4. BLOCK DIAGRAM(DESKTOP)	
5. POWER SEQUENCE BLOCK	
6. DC JACK & VOLTAGE RESET	
7. 3.3V & 5.0V	
8. 1.8V & 2.5V	
9. 1.5V & VTT	
10. VCORE & VCPU	
11. POWER LOW SWITCH	
12. DOVE1	
13. DOVE2	
14. DOVE3	
15. DDR TERMINATOR	
16. DDR MEMORY	
17. DDR MEMORY	
18. DVI TRANSMITTER	
19. DVI CONN	
20. SD & SATA CONN	
21. CONFUGRATION (STRAPPING)	
22. GIGA PHY & RJ45	
23. USB HUB & 9-PIN HEADER	
24. USB PORT & USB CONN	
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26. AUDIO JACK	
27. STATUS LED	
28. POWER UP & RESET	
29. MCU & POWER BUTTON	
30. D-sub	
31. CLOCK GENERATOR	
32. NAND FLASH	
33. TPM	

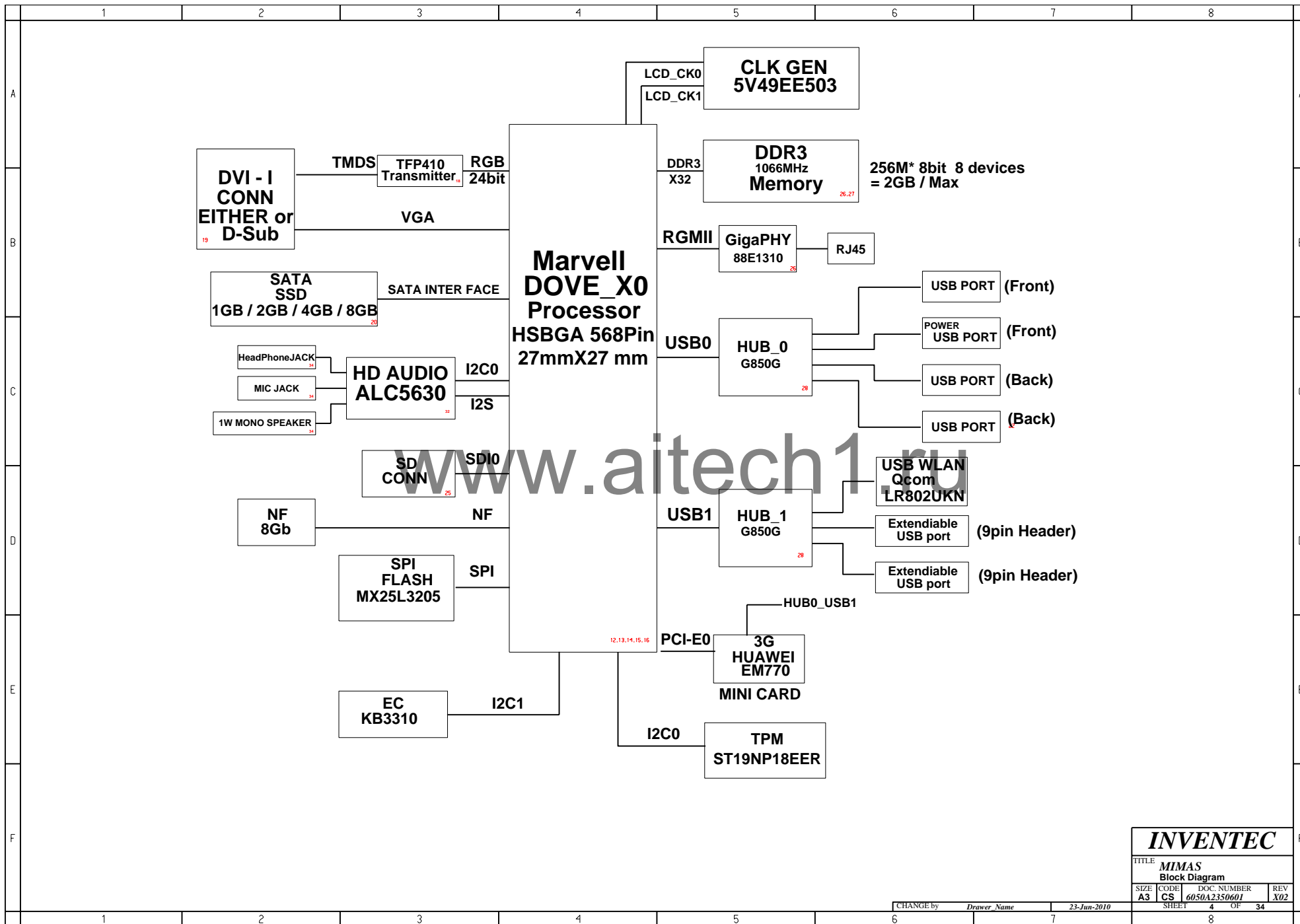
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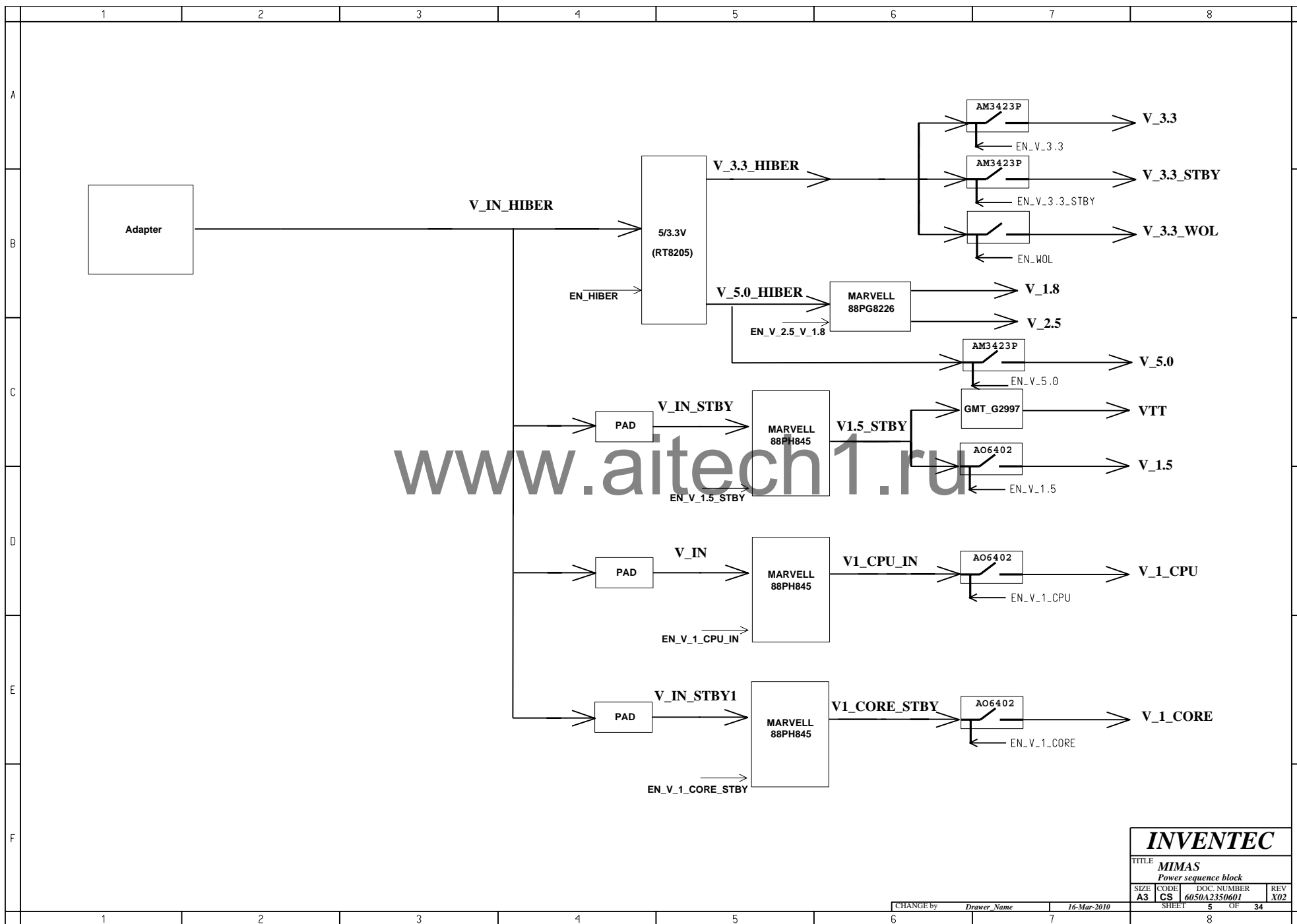
TITLE **MIMAS**

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CHANGE by *Drawer Name* 7-Jun-2010







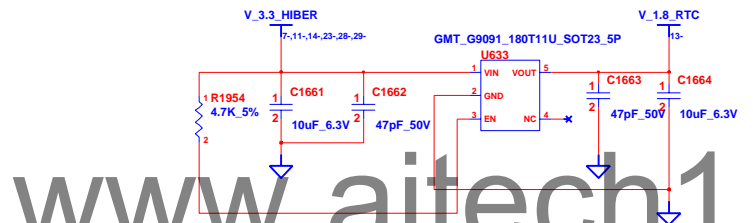
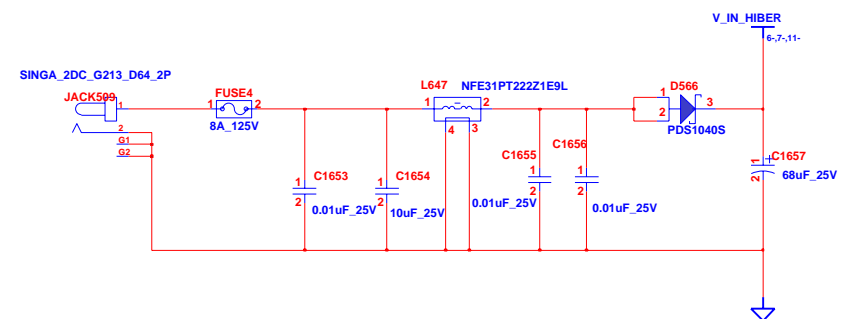
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TITLE: **MIMAS**
Power sequence block

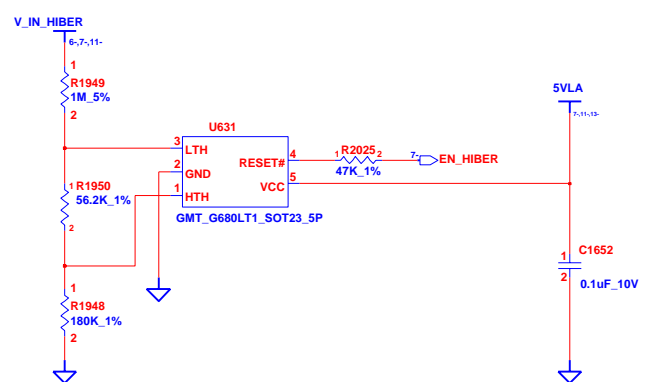
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A2350601	X02

CHANGE by: *Drawer Name* 16-Mar-2010

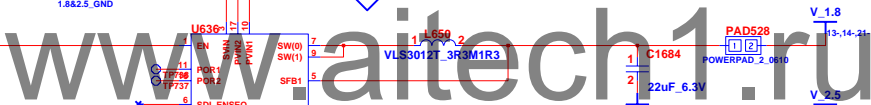
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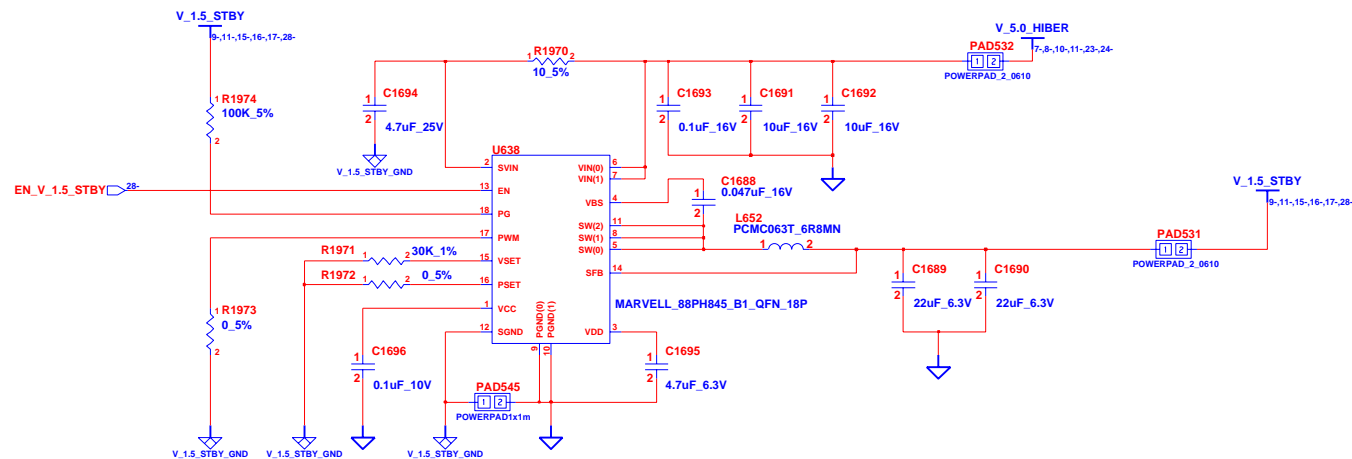
INVENTEC			
TITLE MIMAS DC JACK & VOLTAGE RESET			
SIZE A3	CODE CS	DOC. NUMBER 6050A2350601	REV X02
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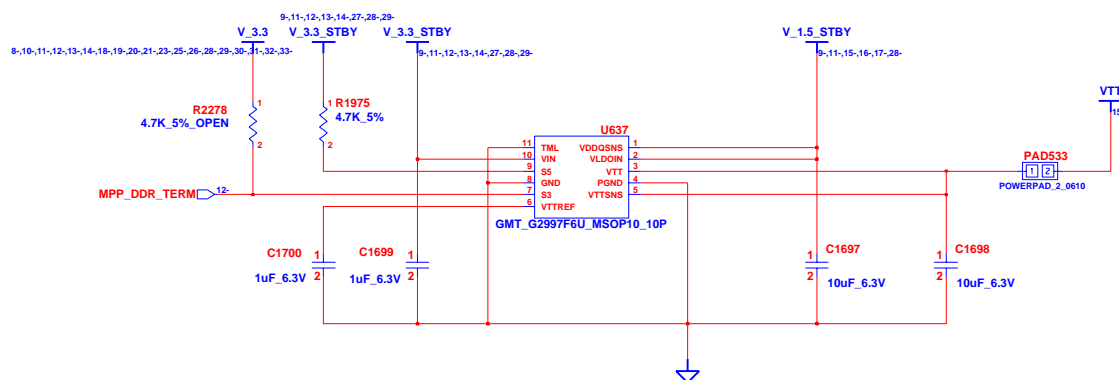
TITLE			
MIMAS 1.8V & 2.5V			
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CHANGE by	<i>Drawer_Name</i>	16-Mar-2010
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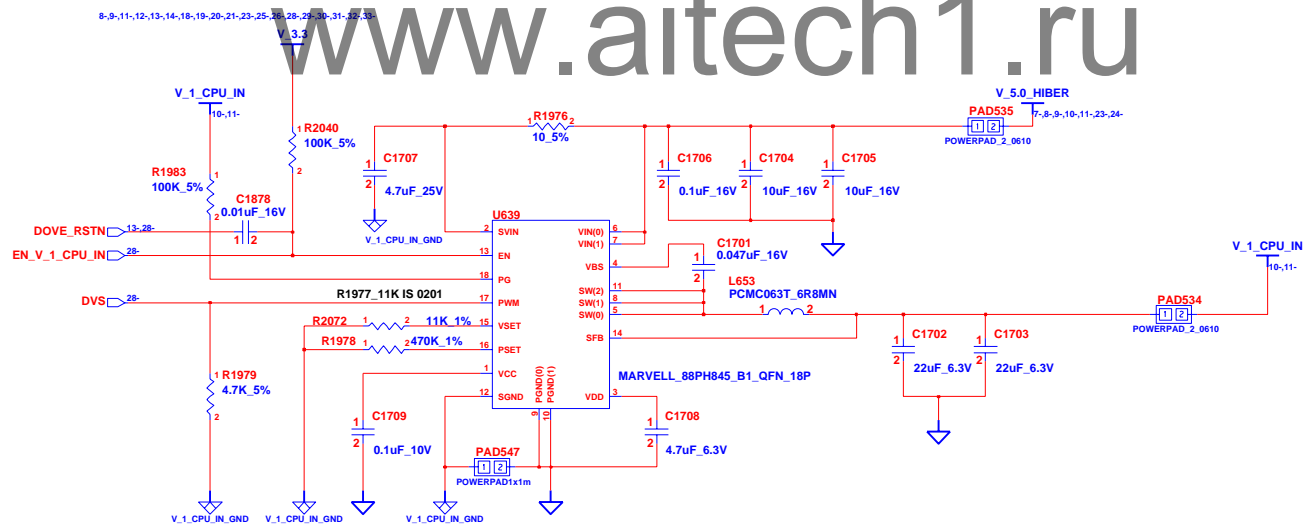
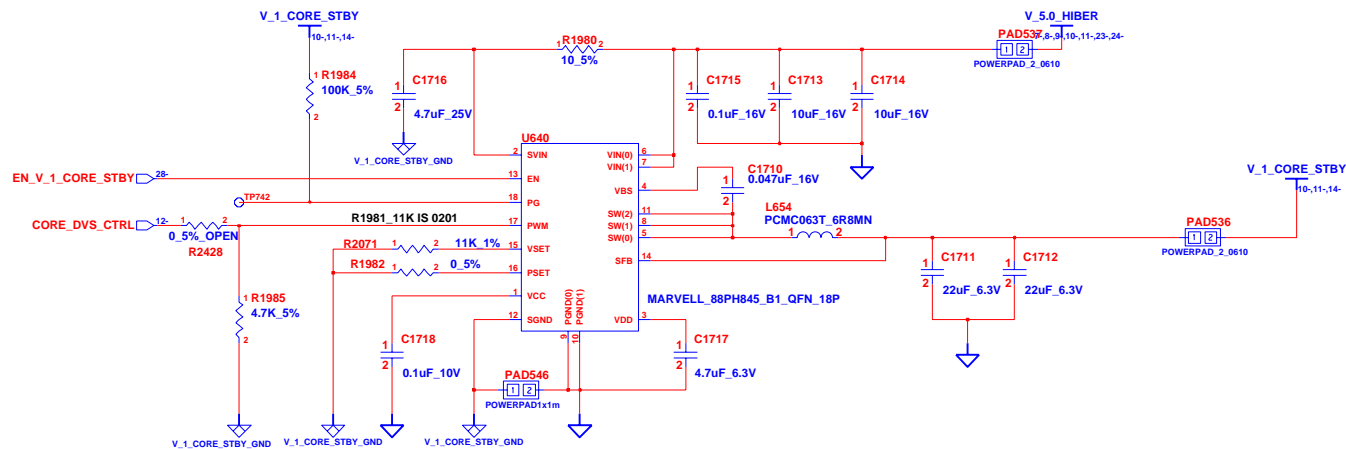


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INVENTEC			
TITLE			
MIMAS			
SYSTEM POWER (3V7 / 1V8, D161)			
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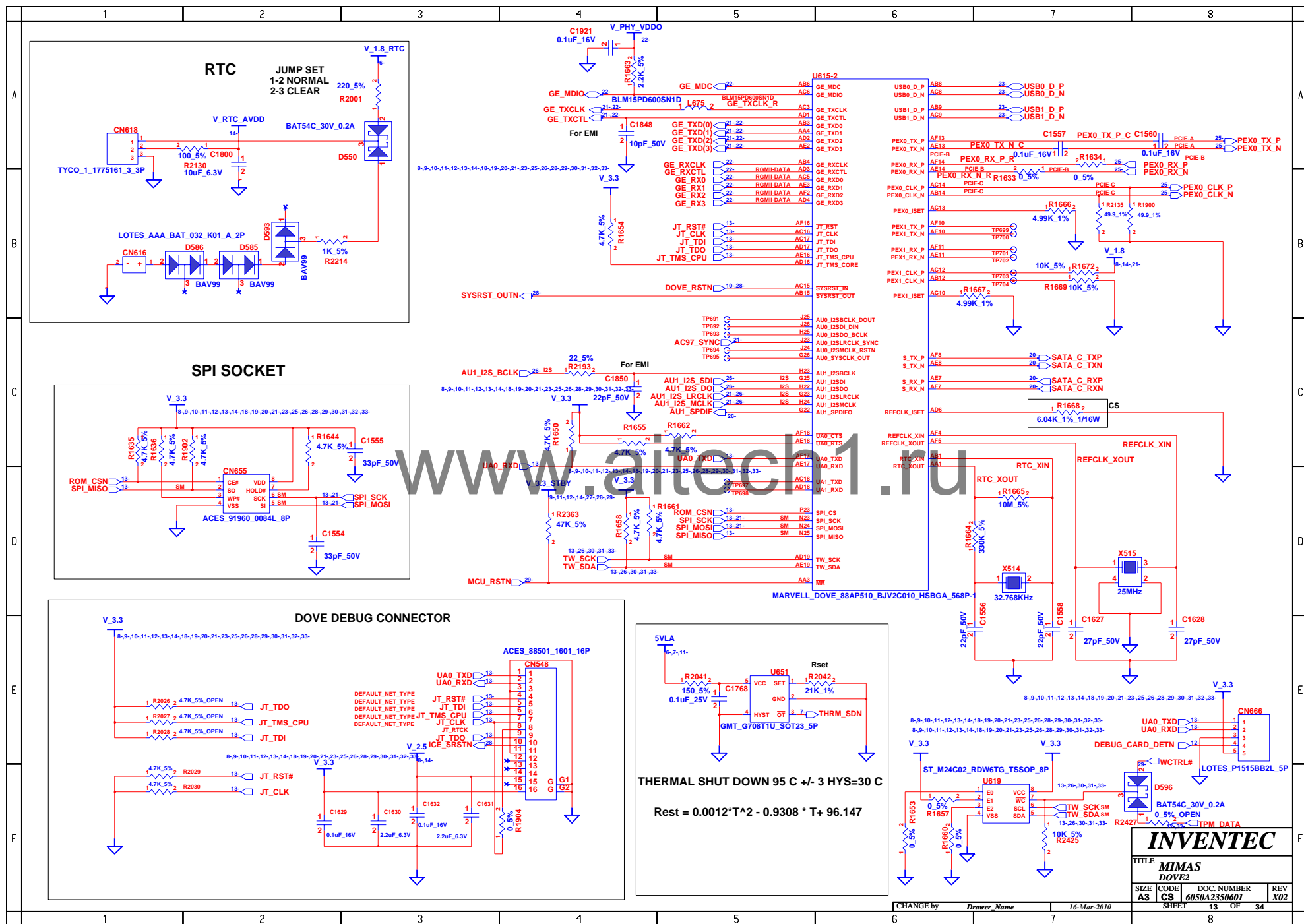


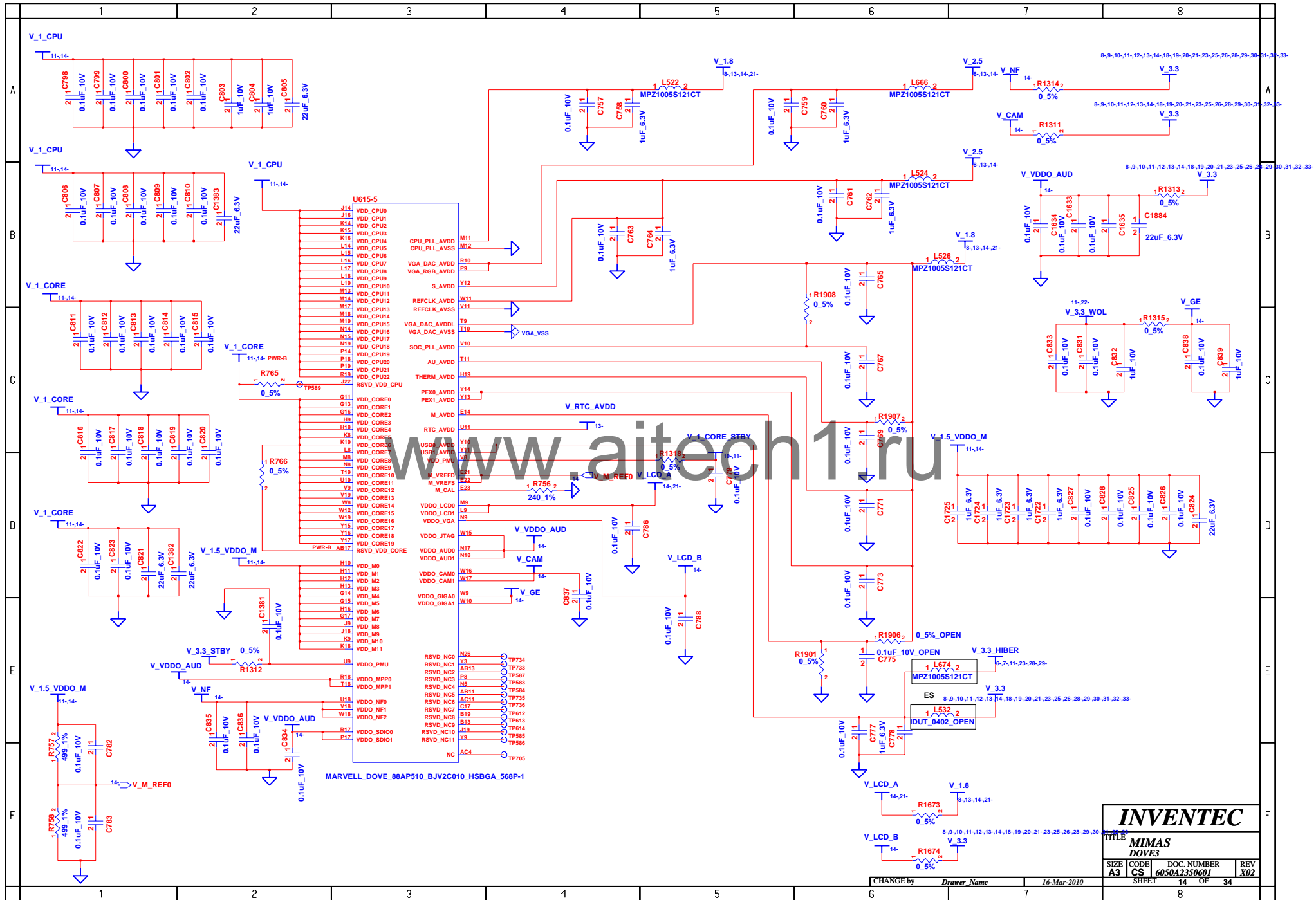
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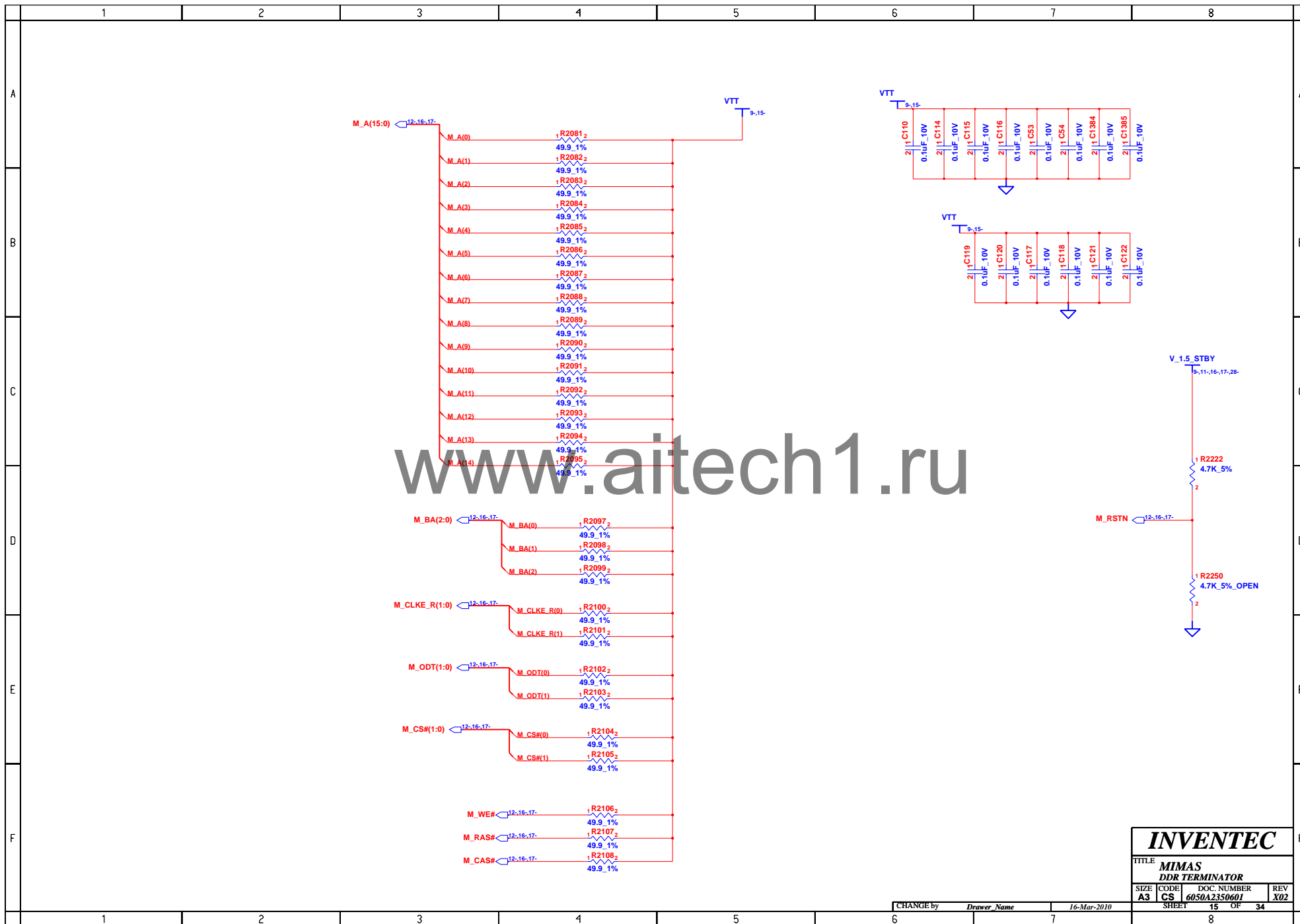
TITLE			
MIMAS			
VCORE & VCPU			
SIZE	CODE	DOC. NUMBER	REV
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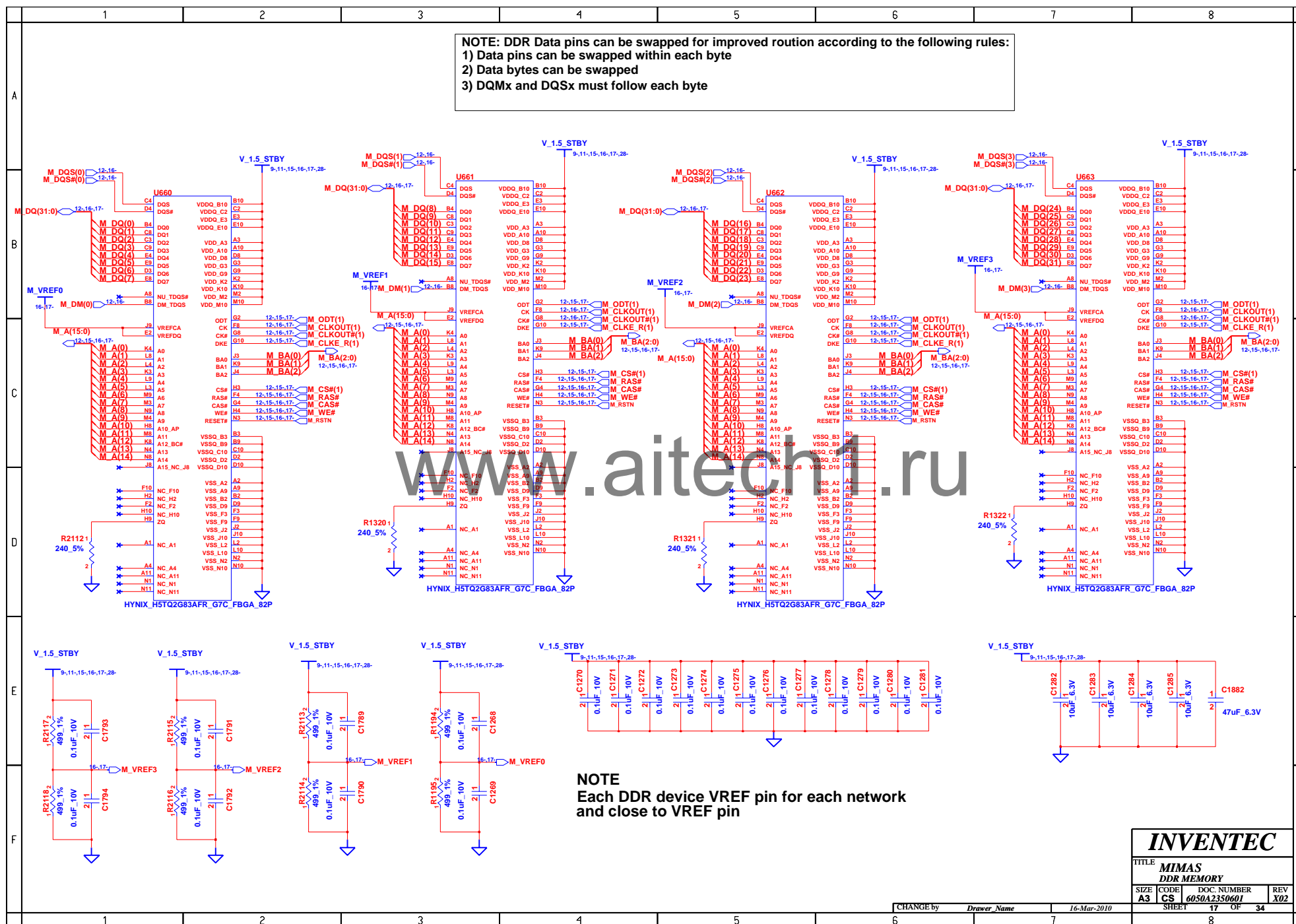
CHANGE by *Drawn Name* 16-Mar-2010

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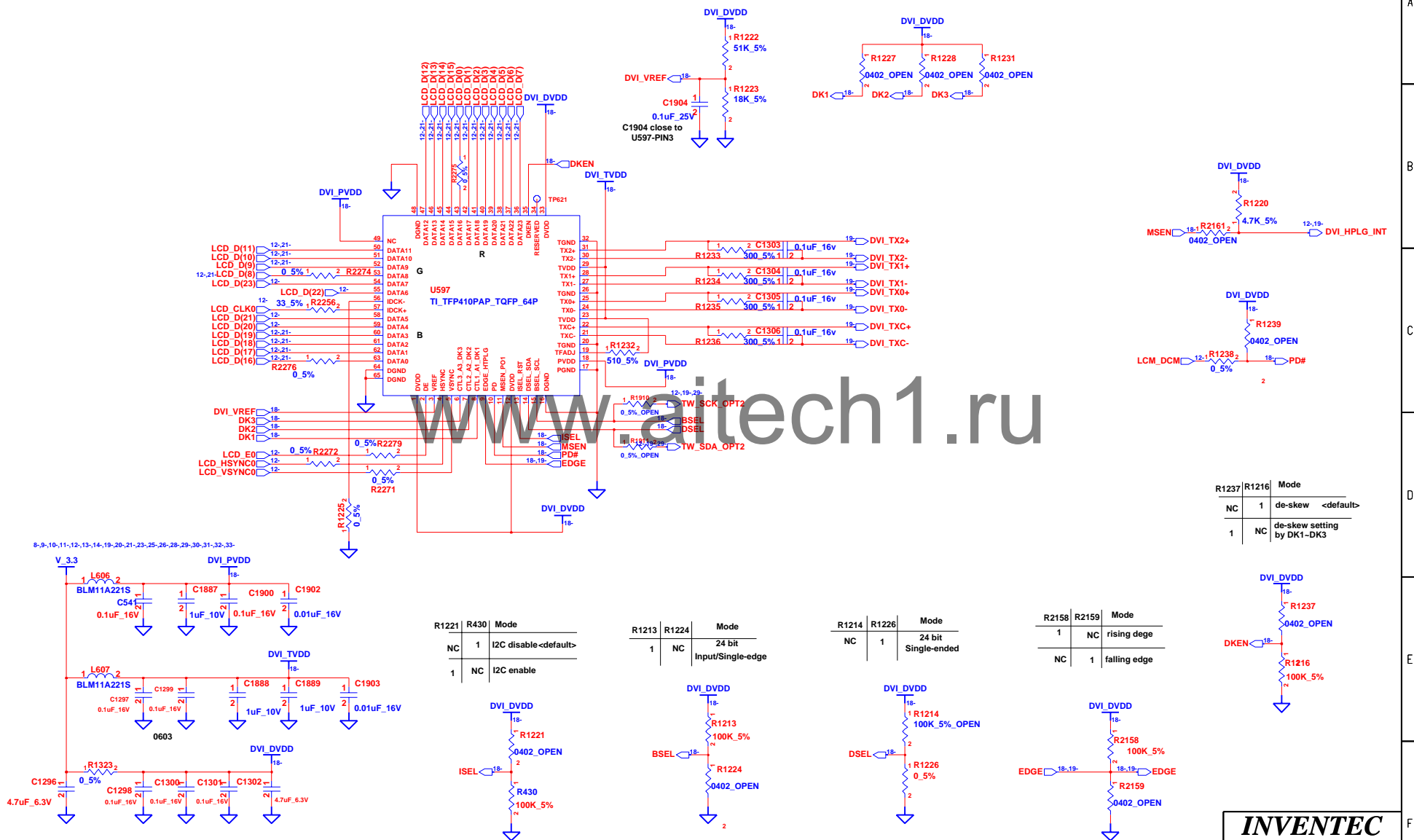






Disable SMBus use strapping setting

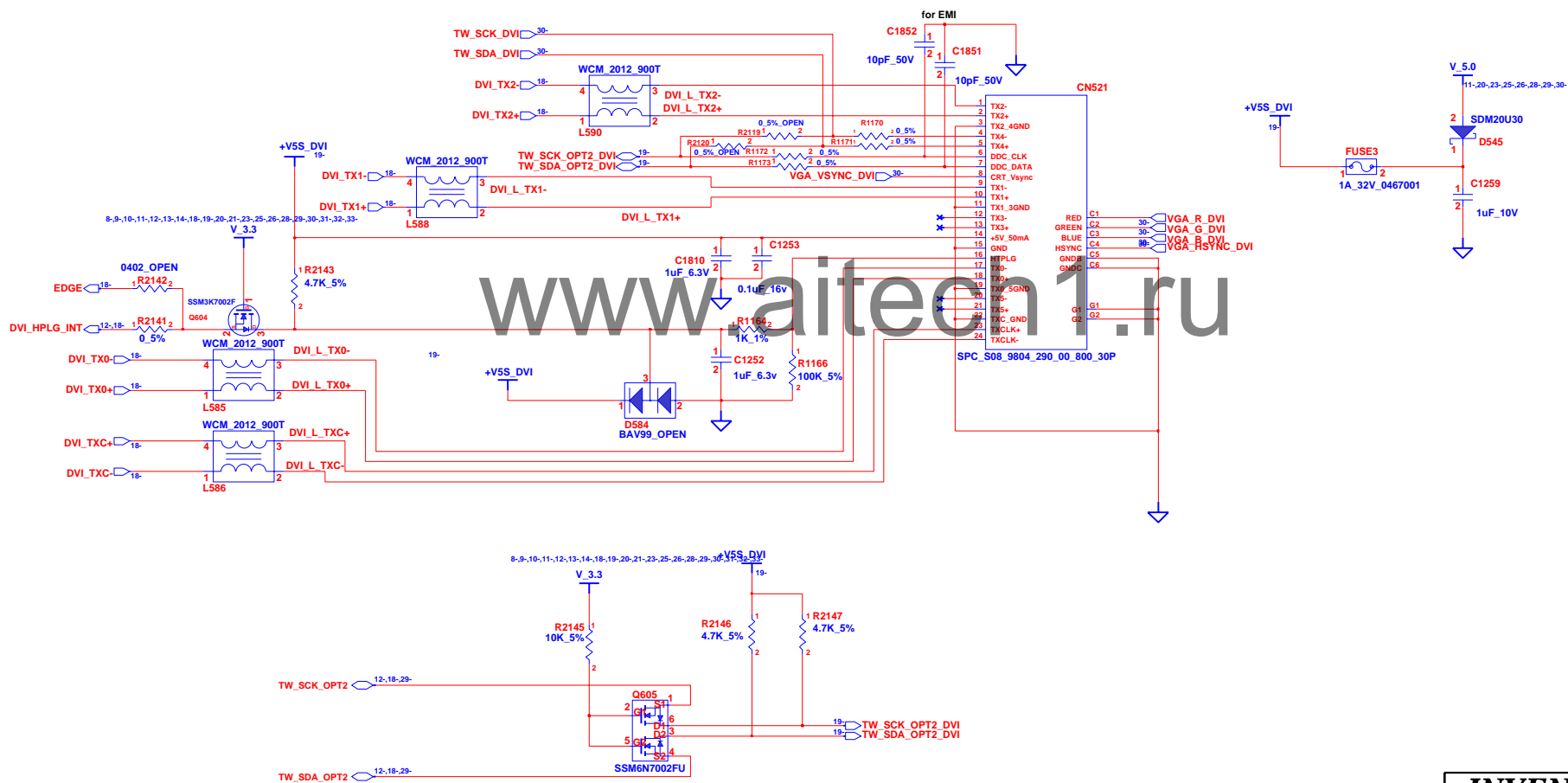
two signal need conn. to CPU GPIO
One is MSEM
The other is EDGE/HTPLG



DVI Transmitter

INVENTEC			
TITLE MIMAS DVI TRANSMITTER			
SIZE A3	CODE CS	DOC. NUMBER 6050A2350601	REV X02

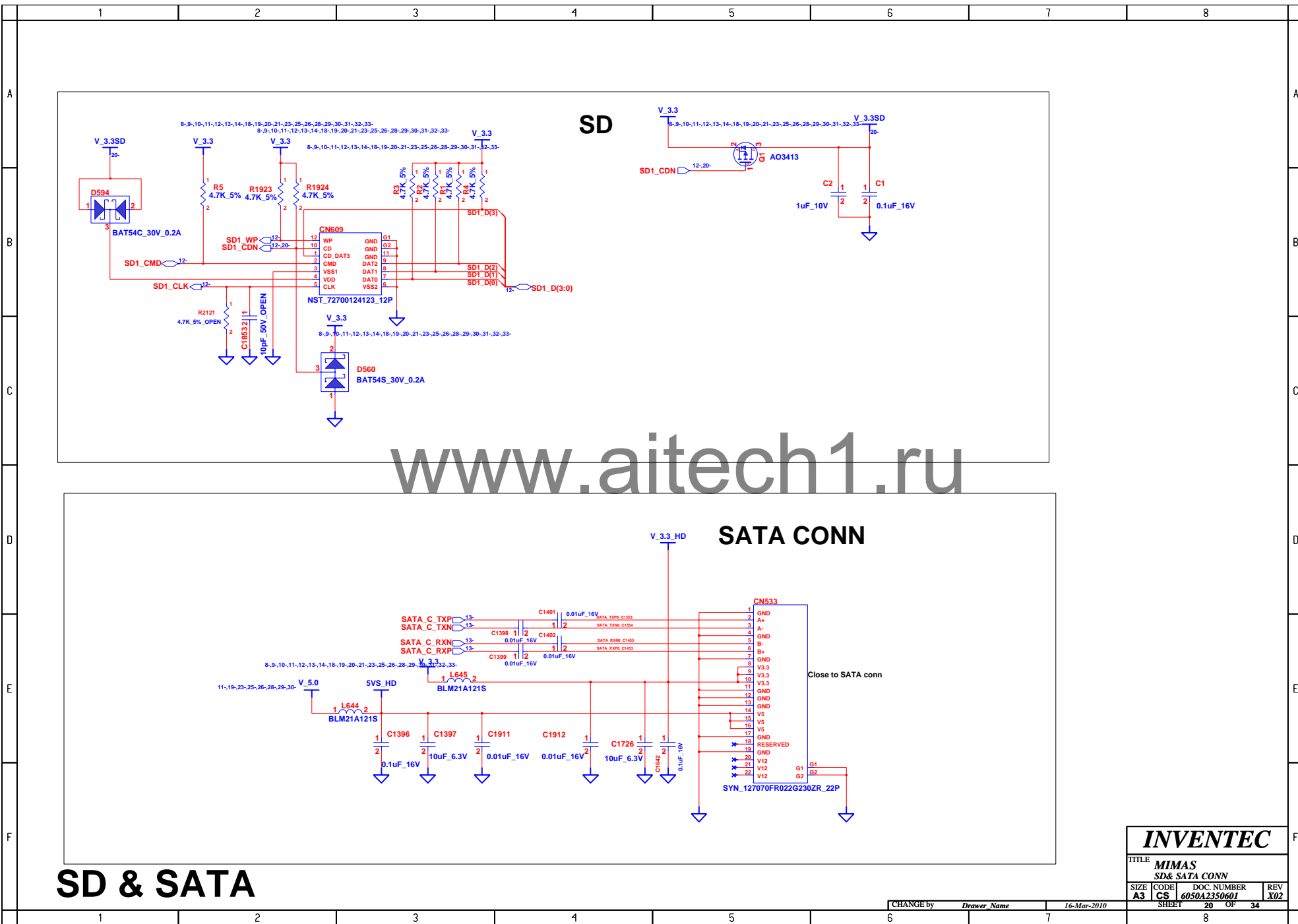
Desktop SKU#2 (DVI ONLY)



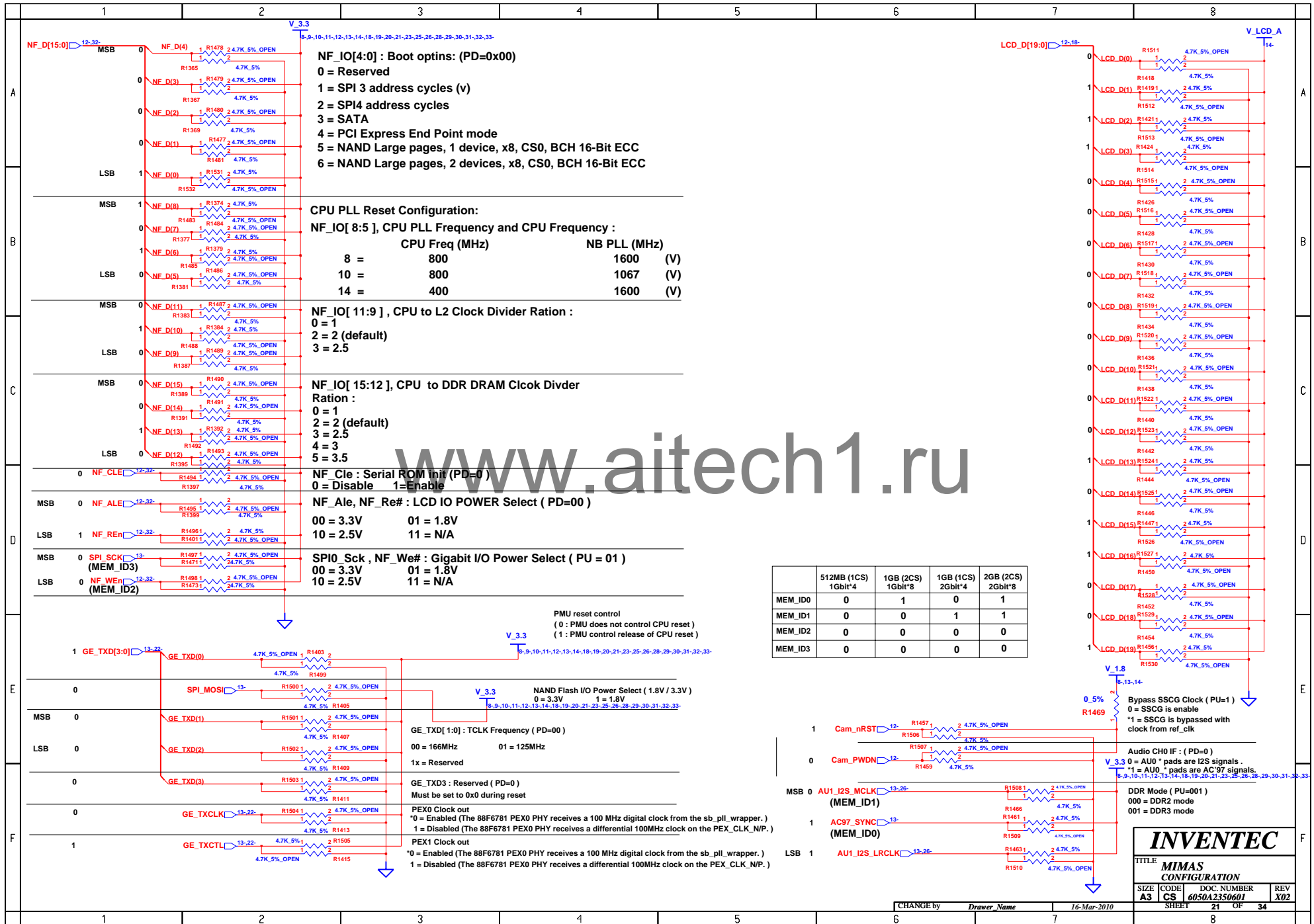
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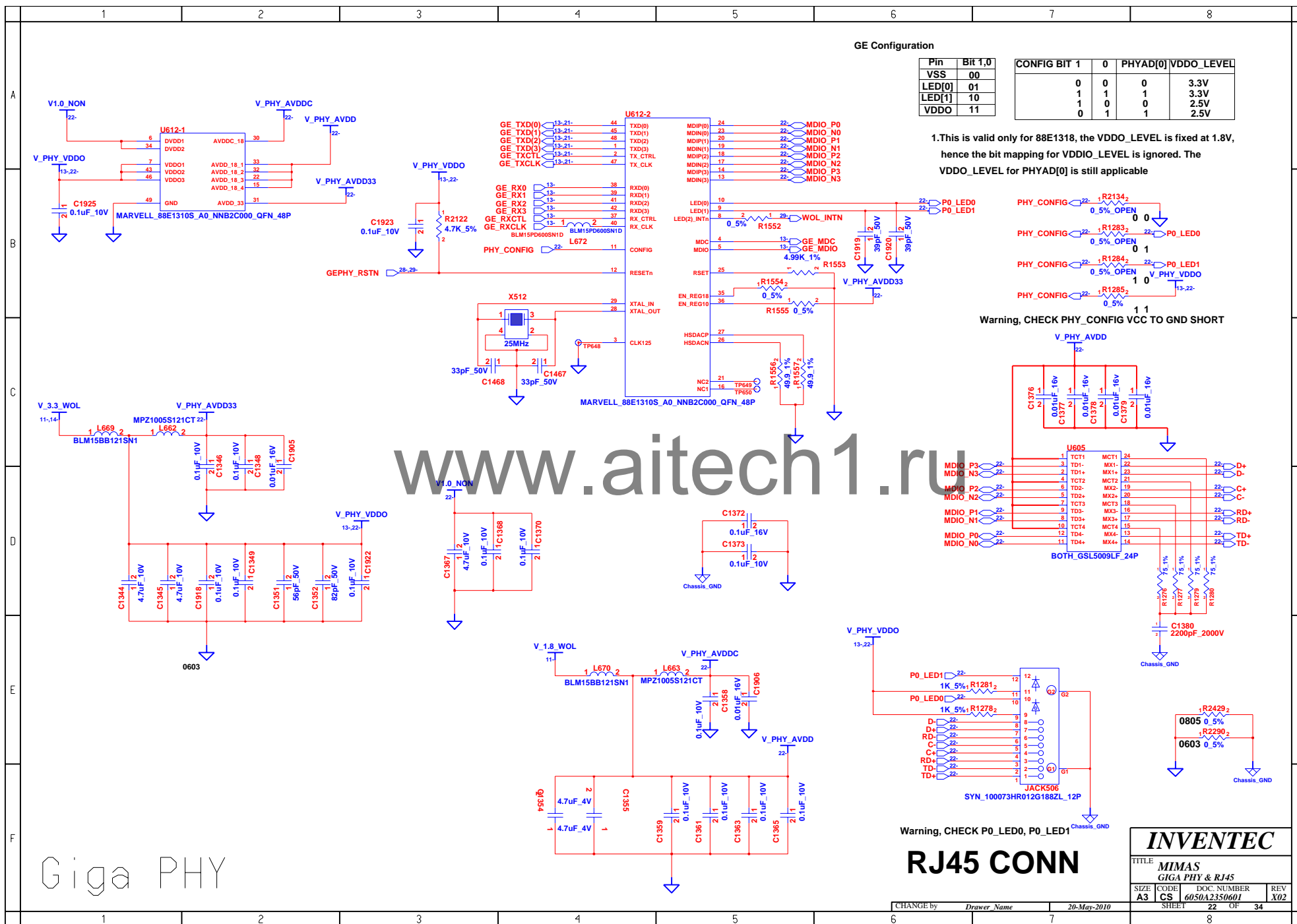
TITLE			
MIMAS DVI CONN			
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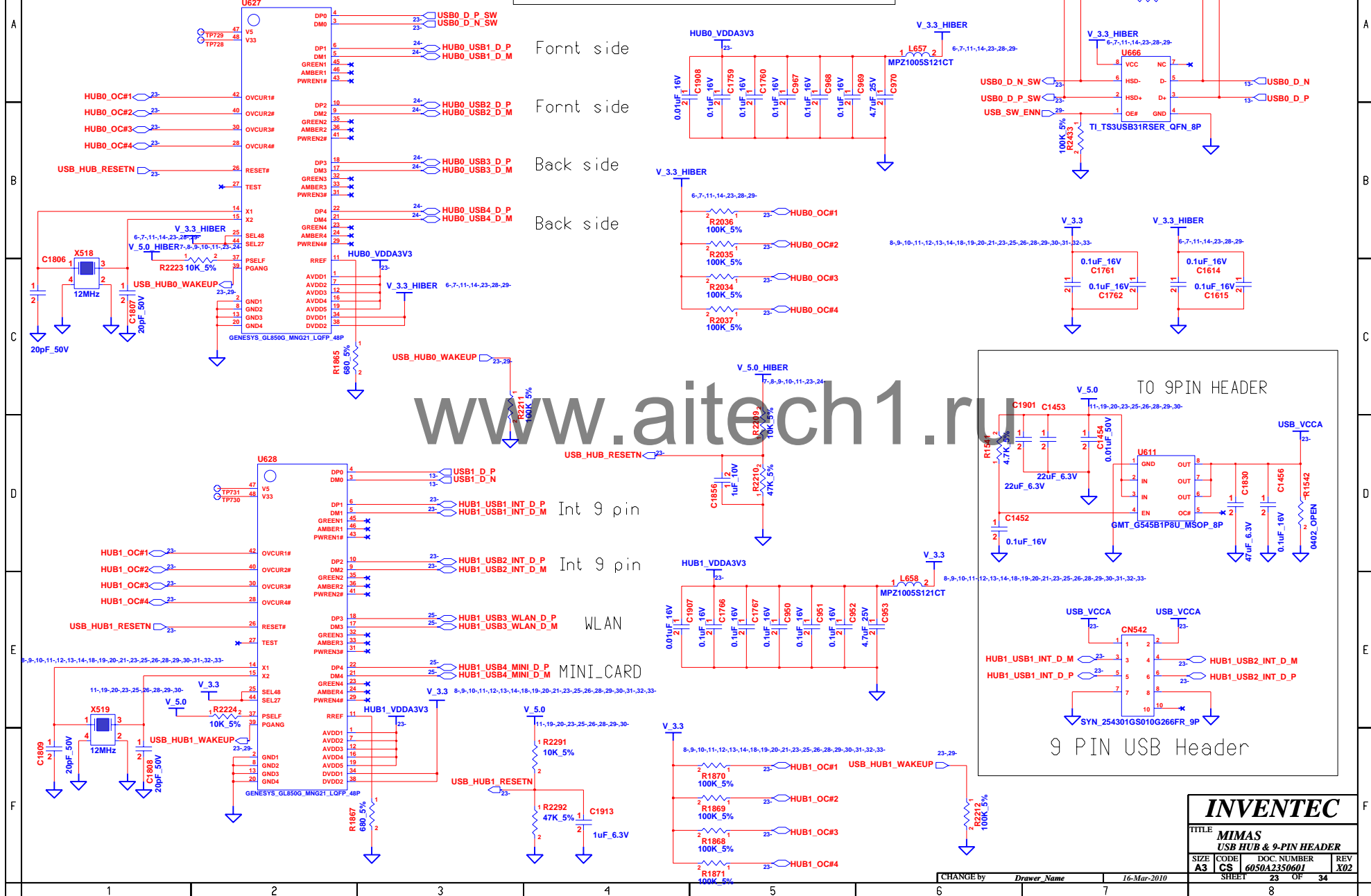
INVENTEC				
TITLE MIMAS SD& SATA CONN				
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Layout Note : Refer to AN15.17 Rev1.1



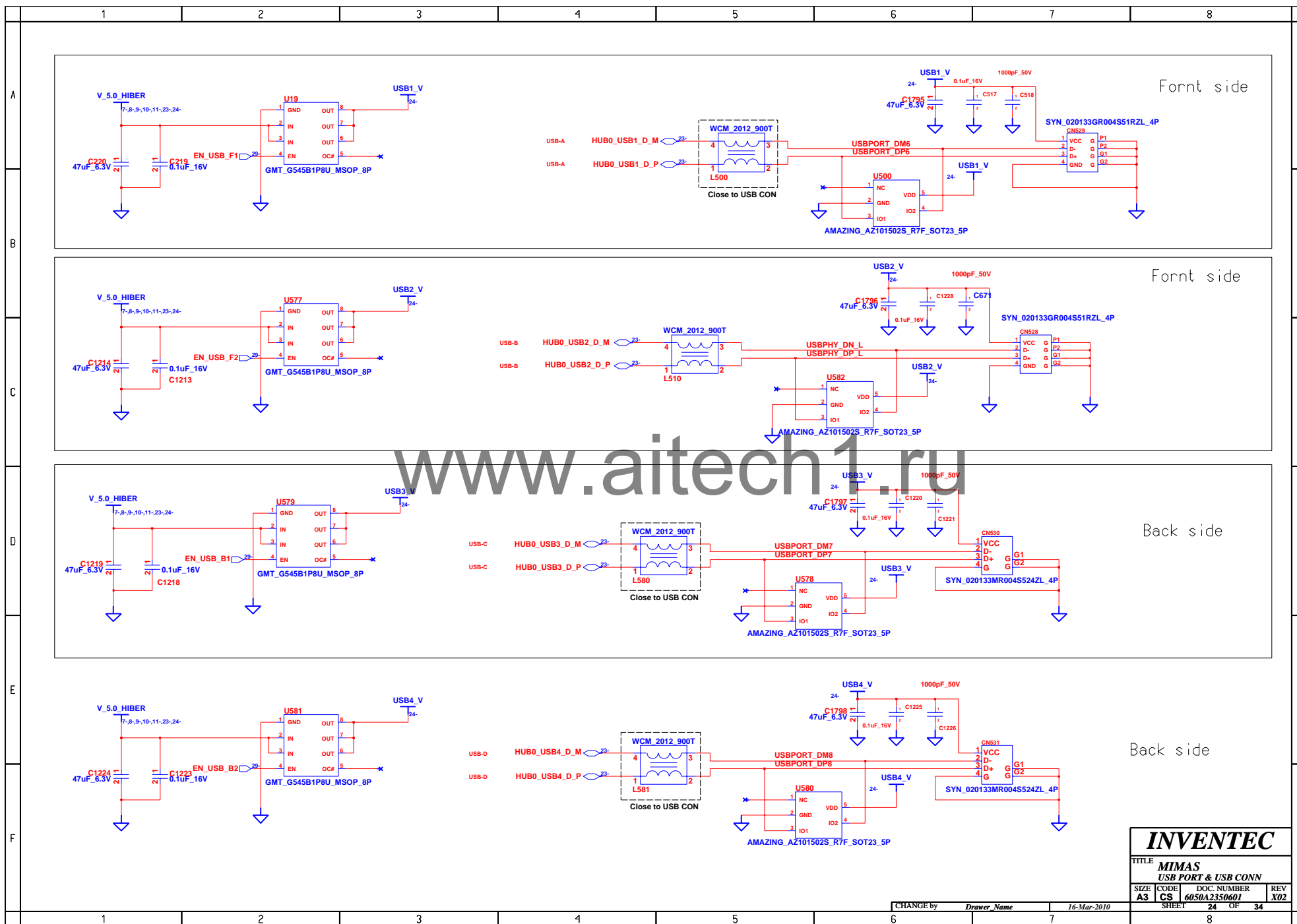
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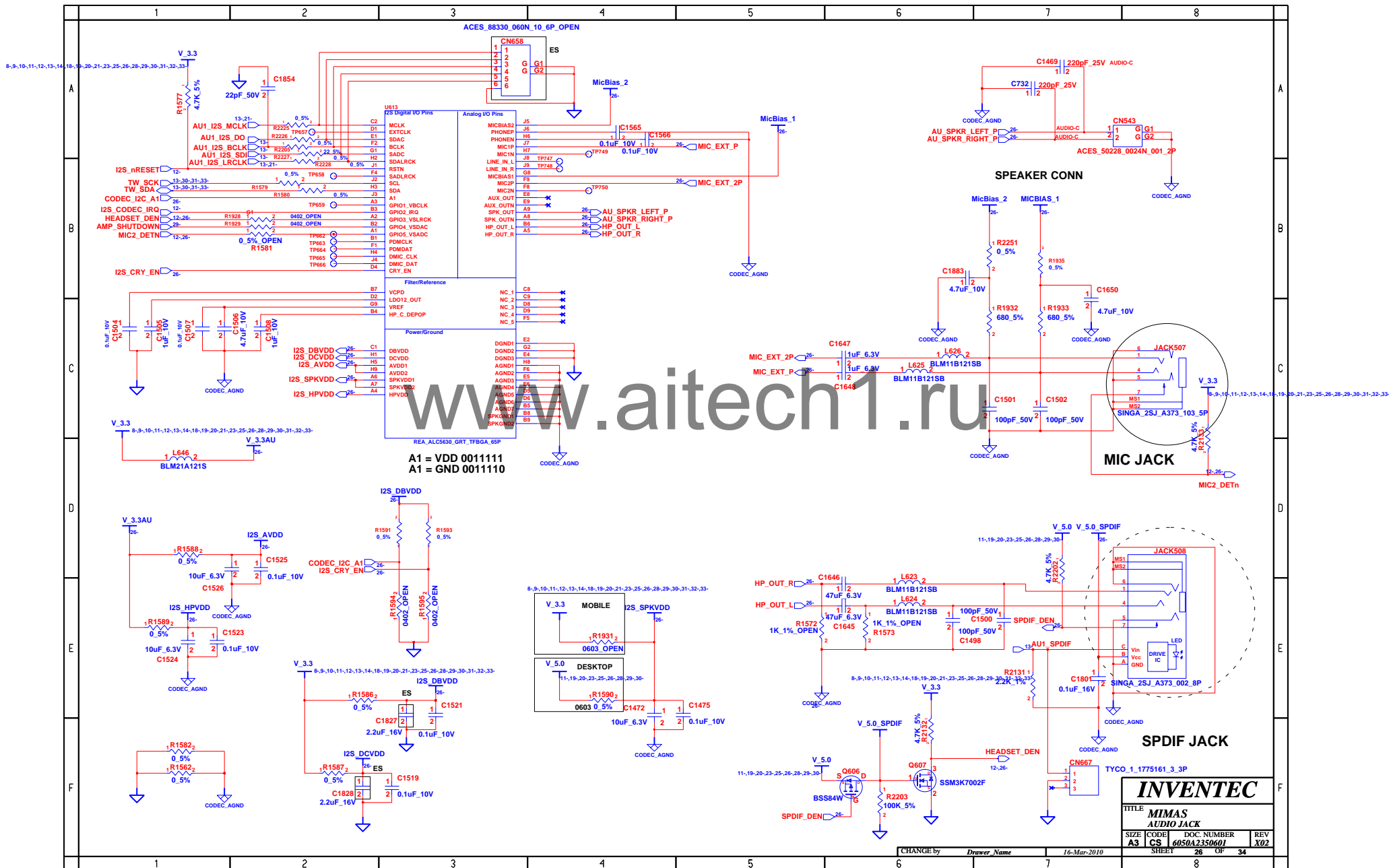
TITLE
MIMAS
USB HUB & 9-PIN HEADER

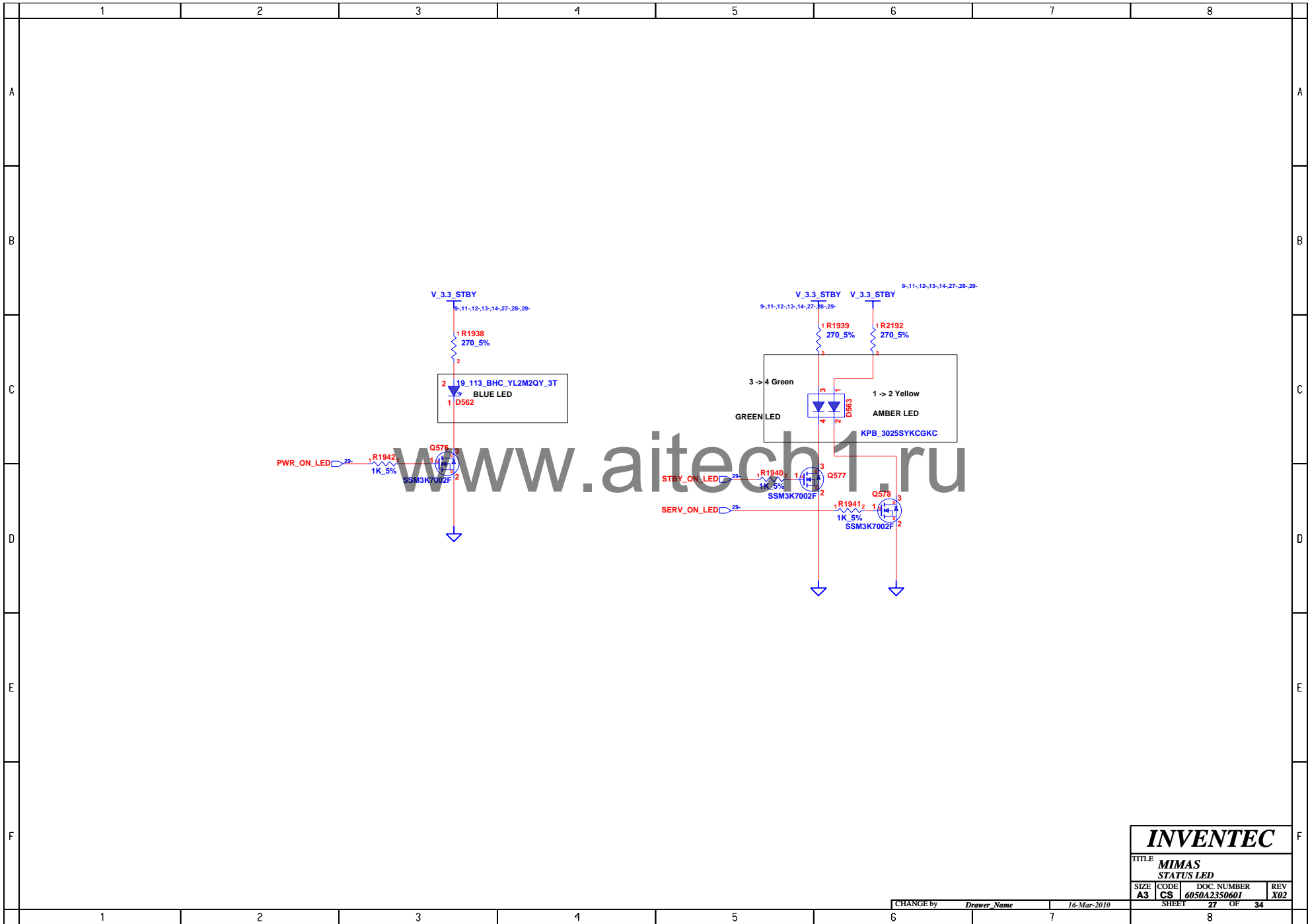
SIZE CODE DOC NUMBER REV
A3 CS 6050A2350601 X02

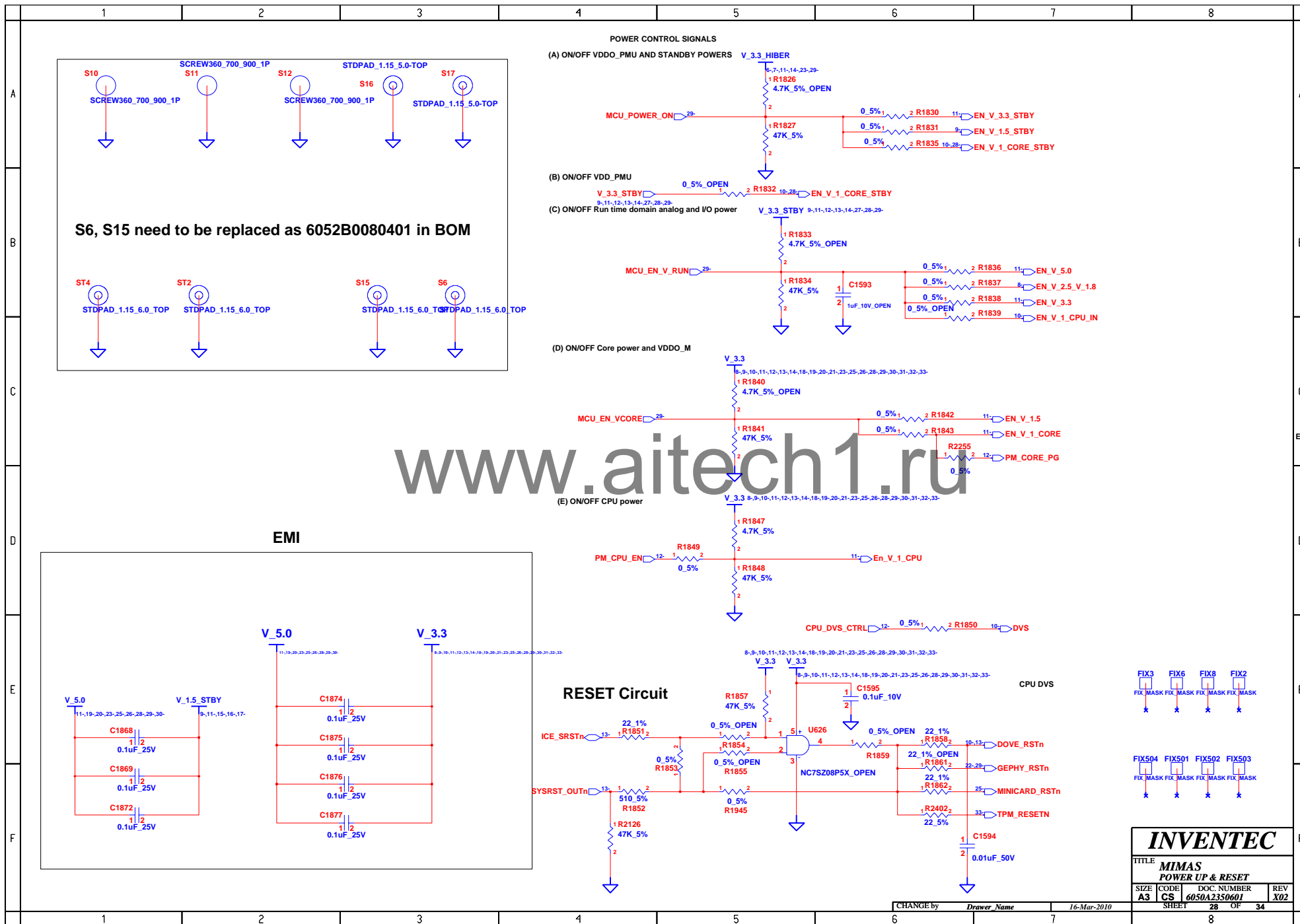
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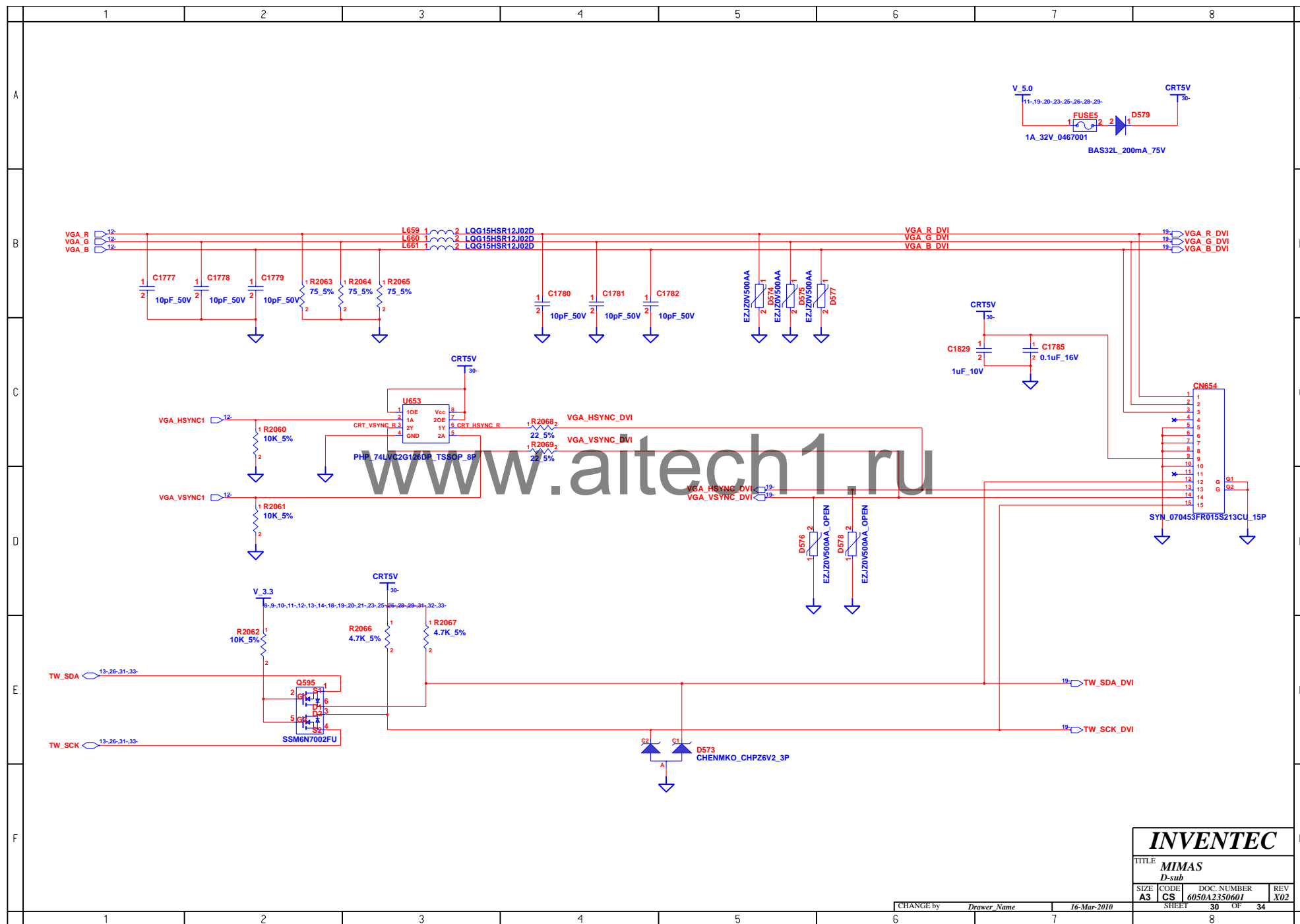




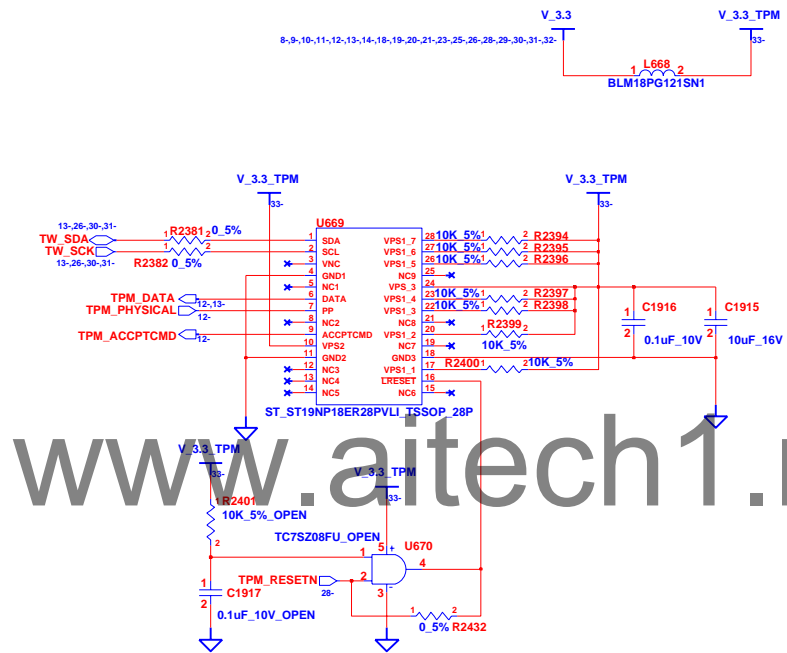
S6, S15 need to be replaced as 6052B0080401 in BOM

EMI

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INVENTEC				
TITLE				
MIMAS				
D-sub				
SIZE	CODE	DOC. NUMBER	REV	
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INVENTEC

TITLE			
MIMAS			
TPM			
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A3	CS		

CHANGE by MIMAS 19-May-2010

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